

ABSTRACT OF THE DISCLOSURE

In an audio amplifier having a D-class power amplifier, a noise upon muting is suppressed. ~~There are provided a~~ A sampling rate converter circuit ~~23~~ for sampling rate converting a digital audio signal ~~S11~~ into a digital audio signal ~~S23~~, and a $\Delta\Sigma$ modulation circuit ~~14~~ for re-quantizing the digital audio signal ~~S23~~ into a bit-reduced digital audio signal ~~S14~~ are provided. Further, ~~there are provided a~~ PWM modulation circuit ~~15~~ for converting the digital audio signal ~~S14~~ into a PWM signal ~~S15~~, and a D-class power amplifier ~~16~~ to which the PWM signal ~~S15~~ is are supplied. Still further, ~~there are provided a~~ dither signal forming circuit ~~18~~ for superimposing a dither signal SDI on the digital audio signal ~~S23~~, and a forming circuit ~~19~~ for forming a muting signal SDET are provided. Upon muting, an input side of the sampling rate converter circuit ~~23~~ is stopped by the muting signal SDET.